Mirabilis Design introduces Internet-enabled VisualSim: a knowledge-oriented environment where content is published, managed and used through network applications.

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Mirabilis Design announces VisualSim Virtual Web; Share system models to be viewed and simulated within a browser and a distributed data management that enables remote sharing of modeling Intellectual Property.

Sunnyvale, CA. — February 13th, 2008— Mirabilis Design Inc. of Sunnyvale, CA today announced VisualSim Virtual Web, an innovative solution that moves system level models to the Web and makes them Internet-enabled. The solution facilitates true collaborative product design and distributed model construction. The solution substantially reduces technical and installation support cost and enables remote locations to participate in design reviews. System and semiconductor companies can now share models with management, customers and contractors without distributing code world-wide or requiring enormous revisions management effort. At the same time, Universities and design houses can share their Intellectual Property without incurring substantial costs.

The Virtual Web consists of two parts. The first technology called VisualSim Explorer enables models to be embedded in design documents, blogs, wikis and other Web-based solutions. Model viewing, simulation and analysis of hardware and software models can be done entirely within the browser from any Internet location without a local software installation. Designers and other collaborative teams can share comments and provide feedback within the same environment. The ability to access the models in the Web Browser introduces architecture exploration and design validation to non-Architects, non-programmers, marketing and field personnel. The second is a collaborative model construction and is fully integrated into the VisualSim Architect. The capability allows model construction in VisualSim Architect using components located anywhere on the Internet without a local download. Accessing development components across the Internet enables wide-reuse and substantially reduces model construction time.

“Web 2.0 has really shown the world how to facilitate creativity, collaboration, and sharing between users. Adopting this approach will enable Architects and system designs to be more innovative and for customers feedback to be available very early in the design cycle,“ said Deepak Shankar, Founder of Mirabilis Design. “To achieve this, we need to change the ways models are constructed and used. VisualSim Virtual Web is leading the drive towards this goal.”

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The Virtual Web combined with VisualSim Architect, the only concept-level system design application in the industry, provides a strong environment for architecture exploration of complex electronics systems for performance, power and behavior. VisualSim provides parameterized, pre-defined modeling libraries that contain models of standard components such as AXI bus and ARM A8 processor; and building blocks to construct custom components such as traffic generators, hardware accelerators and operating environments. The library is capable of capturing models at all levels of Transaction Level Modeling (TLM). This solution is used by Architects, Performance and Systems Engineers to generate requirements, validate design assumptions, create an end-to-end data movement system prototype and conduct performance validation of the implementation. The embedded models can contain components developed using C. C++, Java, SystemC and VisualSim libraries.

Mirabilis Design will be demonstrating the VisualSim Xilinx FPGA Modeling Toolkit and the VisualSim Explorer in the Xilinx Booth at the Embedded World Conference in Nuremberg, Germany on February 27, 2008 between 9:15-12.

Availability

VisualSim Virtual Web has been introduced as two products that are currently shipping and available on Windows, Linux, Solaris, HP/UX and MAC OS. VisualSim Explorer is a licensed server that enables Web-based sharing. A single license can accommodate a large number of concurrent users.

About Us

Founded in 2003, and headquartered in Sunnyvale, CA, USA, Mirabilis Design is a leading provider of System-Level Architecture Exploration software for designing electronics and real-time software. Using VisualSim, designers can architect the “right” product, i.e. one which minimizes product failures and has not been over- or under- designed. Mirabilis Design accelerates Concept Engineering by drastically reducing typical model development from months to days and overall project time by 50%. Our customers are focused in computing, semiconductors, networking and aerospace. The end-users are Project Managers, System Architects, Systems Engineers, Hardware Engineers and Software Engineers. Benefits from the solution are a visual executable specification; easier creation of optimized and differentiated products and; corporate infrastructure enabling extremely fast design trade-offs for price, performance and power.

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